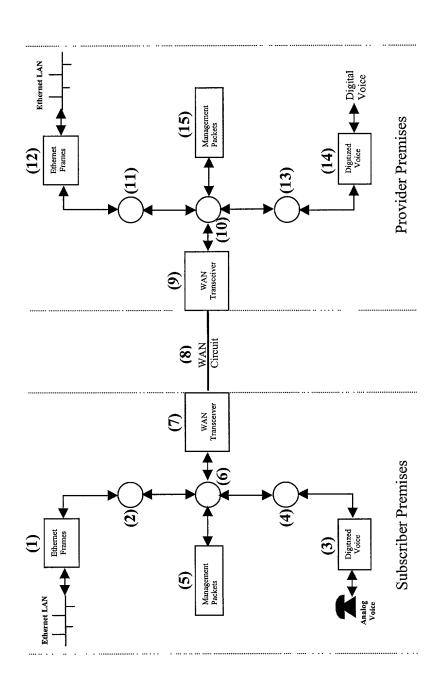
Figure 1. Data Flow Through the System



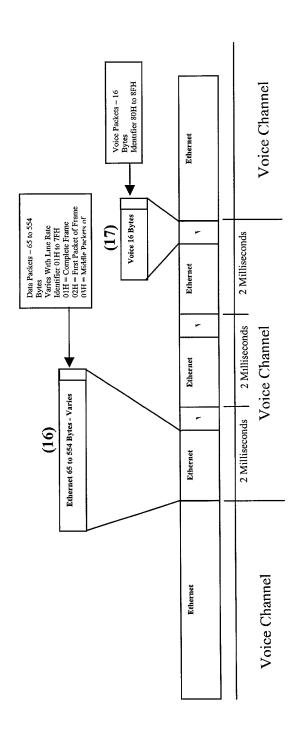


Figure 2. Packet Structure on the WAN Circuit

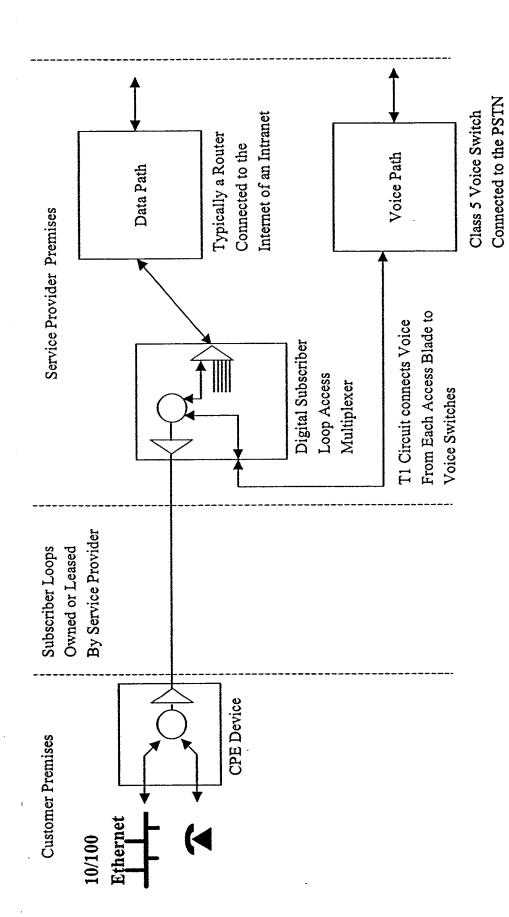


Figure 3

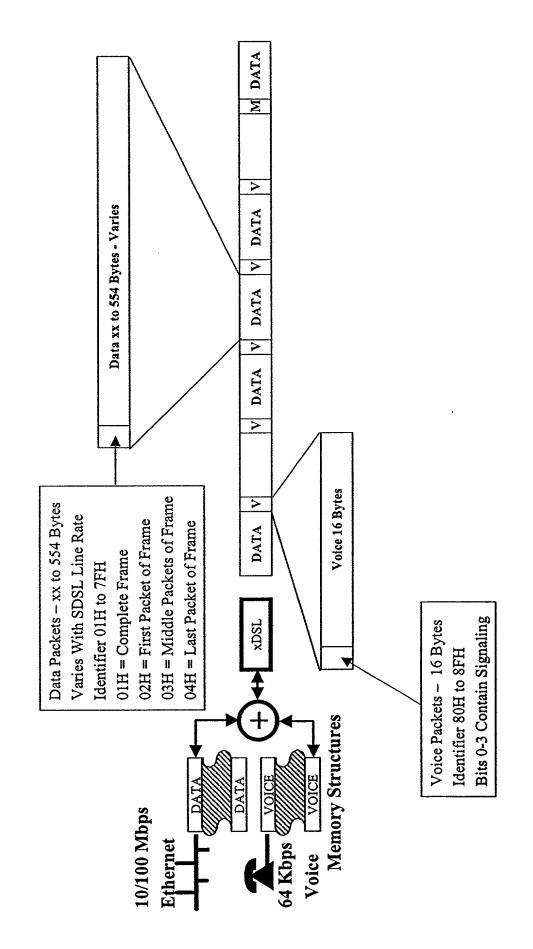


Figure 4

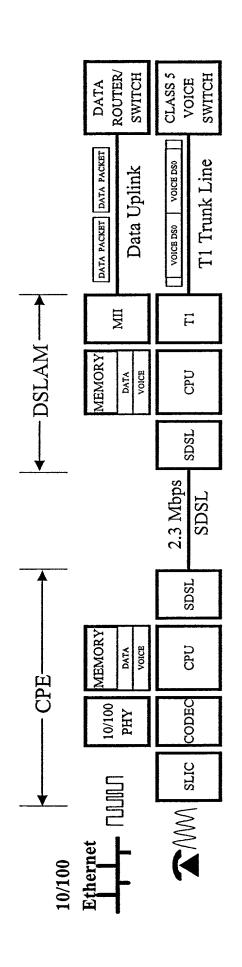


Figure S

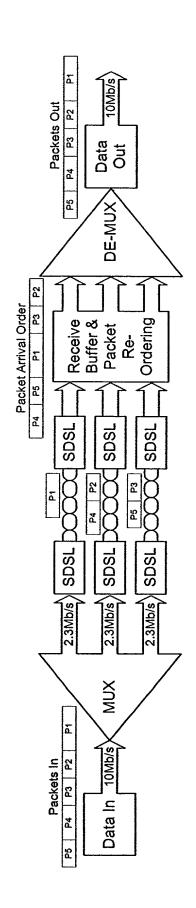


Figure 6

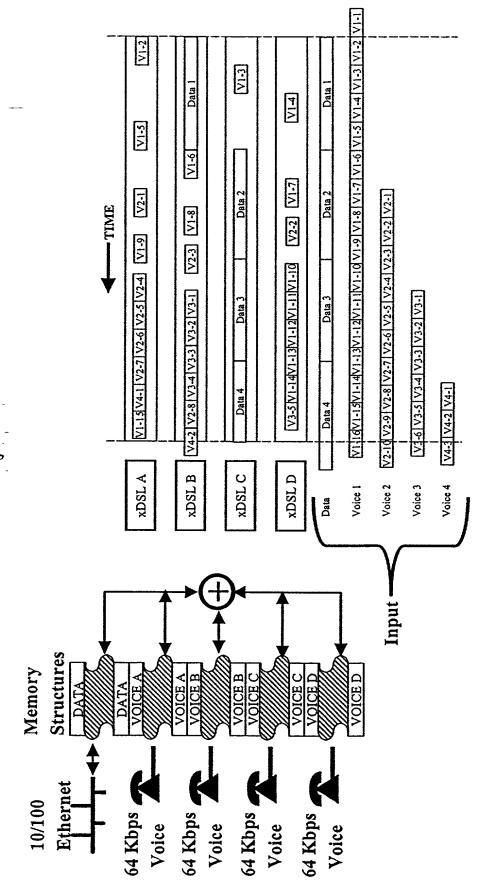


Figure 7

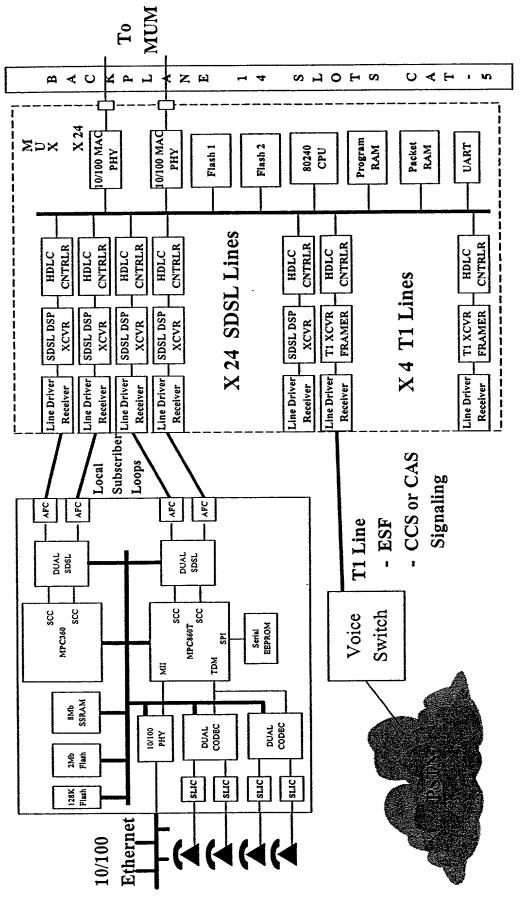


Figure B